

FIG. 1

BORON DOPING PROCESS (p++)

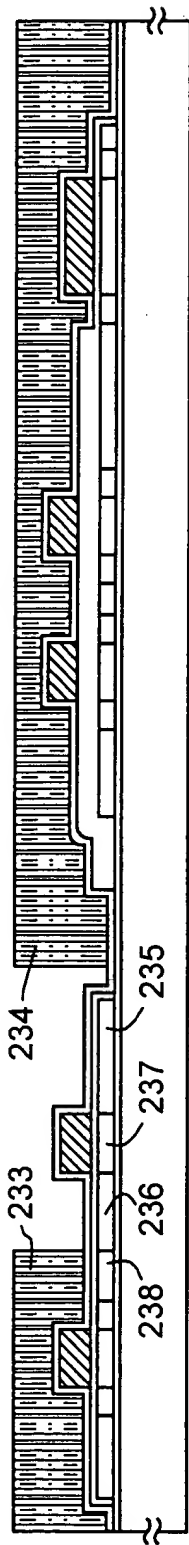


FIG. 3A

GETTERING PROCESS

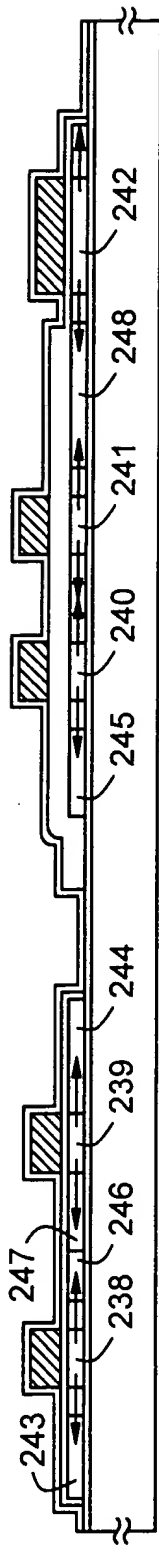
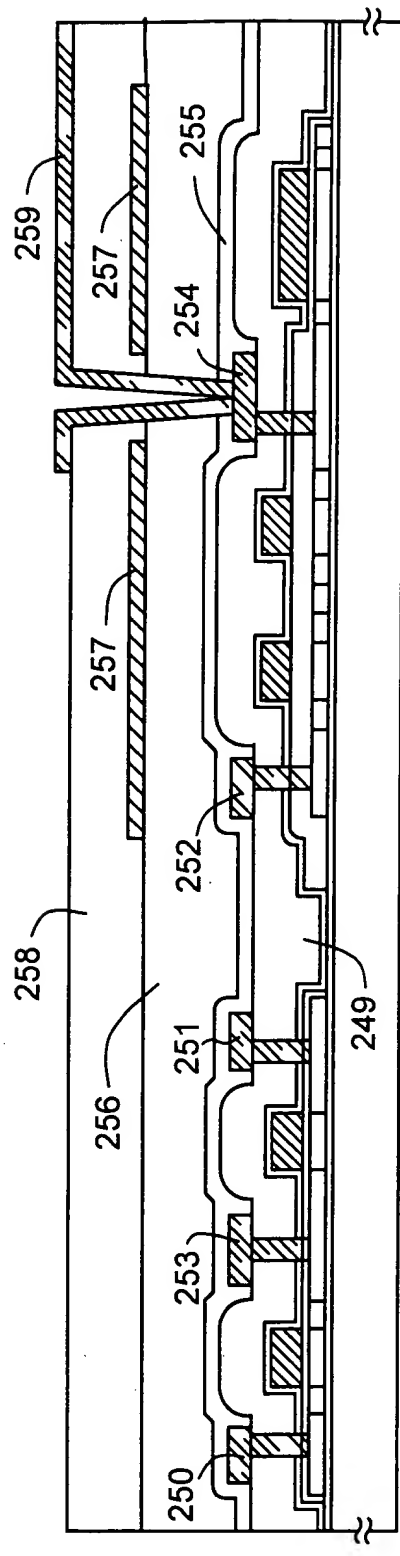


FIG. 3B



DRIVER CIRCUIT

PIXEL MATRIX CIRCUIT

FIG. 3C